THIN FILM TRANSISTOR HAVING LDD REGION AND PROCESS FOR PRODUCING SAME

ABSTRACT OF THE DISCLOSURE

A thin film transistor display includes a driving circuit and an active matrix. The driving circuit comprises a first thin film transistor structure. The first thin film transistor structure includes a first gate, source and drain regions, a first LDD region, a second LDD region and a first channel region between the first and the second LDD regions. The first gate region is disposed over the first channel region, and partially or completely overlies the first and the second LDD regions. The active matrix is controlled by the driving circuit and comprises a second thin film transistor structure. The second thin film transistor structure includes a second gate, source and drain regions, a third LDD region, a fourth LDD region and a second channel region between the third and the fourth LDD regions. The second gate region is disposed over the second channel region and substantially overlaps with neither of the first and the second LDD regions.